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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: PRITCHARD et al.

Attorney Docket No.:
03-2051/LSIIP240

Application No.: 10/791,337

Examiner: TRINH, Michael Manh

Filed: March 1, 2004

Group: 2822

Title: SPACER-LESS TRANSISTOR
INTEGRATION SCHEME FOR HIGH-K GATE
DIELECTRICS AND SMALL GATE-TO-GATE
SPACES APPLICABLE TO SI, SIGE AND
STRAINED SILICON SCHEMES

Confirmation No.: 2401

Entered
w/ RCE
B/19/07

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I hereby certify that this correspondence is being transmitted by facsimile to fax number 571-273-8300 to the U.S. Patent and Trademark Office on October 18, 2006.

Signed: Sue Funchess
Sue Funchess

AMENDMENT B

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Final Office Action dated September 11, 2006, please amend the above-identified patent application as follows:

Amendments to the Claims are reflected in the listing of claims which begin on page 2 of this paper.

Remarks/Arguments begin on page 7 of this paper.

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